

Sector #3

I hereby certify that this correspondence is being filed by depositing it with the United States Postal Service as first class mail in an envelope with sufficient postage and addressed to the Commissioner of Patents and Trademarks, Washington, D.C. 20231 on the date indicated below.

By Peter K. Trzyna
Peter K. Trzyna (Reg. No. 32,601)

Date January 9, 2001

PATENT

Paper No. 3

File No. Contcir-2

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Inventors	:	Brian J. McDermott, Jr., Daniel McGowan, Ralph Leo Spotts, and Sid Tryzbiak
Serial No.	:	09/694,099
Filed	:	October 10, 2000
For	:	IMPROVED CIRCUIT BOARD OR OTHER MULTILAYER ELECTRICAL DEVICE MADE BY FORMING TEETH TO JOIN LAYERS
Group Art Unit	:	3729
Examiner	:	

Assistant Commissioner of Patents
Washington, D.C. 20231

TRANSMITTAL LETTER

SIR:

Transmitted herewith for filing in the above-identified patent application is the following:

1. Response to Notice to File Missing Parts, applicant claims small entity status;
2. Notice of File Missing Parts;
3. Request for Corrected Filing Receipt; and
4. A copy of the Filing Receipt with corrections thereon.

The Commissioner is hereby authorized to charge any fees associated with the above-identified patent application or credit any overcharges to Deposit Account No. 50-0235.

Please direct all correspondence to the undersigned at the address given below.

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Respectfully submitted,

Date: January 9, 2001

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